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| Carol Diehl | (Depositor's name) |
| <i>Carol Diehl</i> | (Signature) |
| 10/3/07 | (Date) |

| APPLICATION NO. | PIILING DATE | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO. | CONFIRMATION NO. |
|-----------------|--------------|----------------------|---------------------|------------------|
| 10/773,541 | 02/06/2004 | Yutao Ma | 188122000700 | 6489 |

TITLE OF INVENTION: MODEL STAMPING MATRIX CHECK TECHNIQUE IN CIRCUIT SIMULATOR

| APPLN. TYPE | SMALL ENTITY | ISSUE FEE DUE | PUBLICATION FEE DUE | PREV. PAID ISSUE FEE | TOTAL FEE(S) DUE | DATE DUE |
|----------------|--------------|---------------|---------------------|----------------------|------------------|------------|
| nonprovisional | NO | \$1400 | \$300 | \$0 | \$1700 | 10/15/2007 |

| EXAMINER | ART UNIT | CLASS-SUBCLASS |
|--------------------|----------|----------------|
| GARBOWSKI, LEIGH M | 2825 | 716-004000 |

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Cadence Design Systems, Inc

San Jose, California

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DATE: October 3, 2007

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| Number of pages with cover page: | 03 |
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Preparer of this slip has confirmed that facsimile number given is correct: 10475/cxd7

Comments:

Official Filing

Docket No.: 188122000700
Art Unit: 2825
Examiner: L. Garbowski
U.S. Patent Application Serial No.: 10/773,541
Filing Date: February 6, 2004
Inventor(s): Yutao MA et al.
Title: MODEL STAMPING MATRIX CHECK TECHNIQUE IN CIRCUIT SIMULATOR

Papers enclosed herewith:

1. Part B – Fees + duplicate for fee processing (2 pages)

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